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PTO/SB/21 (09-04) Approved for use through 07/31/2006.OMB 0651-0031 U.S. Patent and Trademark Office;U.S. DEPARTMENT OF COMMERCE ter the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number 10,603,004 STENT & TRA Filing Date TRANSMITTAL 06/24/2003 First Named Inventor **FORM** Seungug Koh Art Unit 2874 Examiner Name Phan T.H. Palmer (to be used for all correspondence after initial filing) Attorney Docket Number Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Fee Transmittal Form Drawing(s) Appeal Communication to Board Fee Attached Licensing-related Papers of Appeals and Interferences Appeal Communication to TC ✓ Petition (Appeal Notice, Brief, Reply Brief) Amendment/Reply Petition to Convert to a Proprietary Information After Final Provisional Application Power of Attorney, Revocation Status Letter Change of Correspondence Address Affidavits/declaration(s) Other Enclosure(s) (please Identify Terminal Disclaimer **Extension of Time Request** below): Request for Refund **Express Abandonment Request** CD, Number of CD(s) Information Disclosure Statement Landscape Table on CD Certified Copy of Priority Remarks Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name Signature Printed name Seungug Koh

	CERTIFICATE OF TRANSMISSI	ION/MAILING
I hereby certify that this correspondence sufficient postage as first class mail in the date shown below:	e is being facsimile transmitted to the USPTo an envelope addressed to: Commissioner for	O or deposited with the United States Postal Service with r Patents, P.O. Box 1439, Nexandria, VA 22313-1450 on
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Date

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PTO/SB/17 (12-04v2)
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Fees pursuant to the Consolida	lated Appropriations		Application Nu		10/603,004	
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	r FY 200		First Named In		Seungug Koh	
			Examiner Nam		Phan T.H. Palme	er ·
Applicant claims small	entity status. Se	e 37 CFR 1.27	Art Unit		2874	
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FEE CALCULATION						
1. BASIC FILING, SEAR			2011 5550		""'ATION FEEO	
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Utility		50 500		200		
Design		00 100	• •	130		
Plant	200 10	00 300	150	160	80	
Reissue	300 15	50 500	250	600	300	
Provisional	200 10	00 0	0	0	0	
2. EXCESS CLAIM FEE	:S				Fee (\$)	Small Entity
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3. APPLICATION SIZE F	FEE	1100 1		1 1		
If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50						
sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
Total Sheets Extra Sheets Number of each additional 50 or fraction thereof Fee (\$) Fee Paid (\$)						
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4. OTHER FEE(S)						Fees Paid (\$)
Non-English Specific	cation, \$130 t	ree (no small entity	discount)			

SUBMITTED BY			
Signature	Somo Fol	Registration No. (Attorney/Agent)	Telephone (408) 739-8120
Name (Print/Type)	Seungug Koh	•	Date 07/18/2005

Other (e.g., late filing surcharge):

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Serial Number:

10/603,004

Appn. Filed:

06/24/2003

Applicant(s): Appn. Title:

Seungug Koh et al Micro-Opto-Electro-Mechanical Waveguide Switches

Examiner/AU:

Phan T. H. Palmer / 2874

Mailed:

07/18/2005

At:

Sunnyvale, CA

Amendment A

Assistant Commissioner for Patents Washington, District of Columbia 20231

Sir:

In response to the Office Letter mailed March 22, 2005, please amend the above application as follows:

Specification: Add the following cross-reference information after the Title:

CROSS REFERENCE TO RELATED APPLICATION

This application is a continuation of U.S. patent application Ser. No. 09/635,043, filed August 4, 2000, now abandoned.

Claims: Amend the claims of record as follows:

1. (<u>Currently Amended</u>) A Micro-Opto-Electro-Mechanical Waveguide Switch (MOEM-WS) comprising:

a micromachined planar lightwave circuit (PLC) substrate having at least one integrated optical waveguide fabricated thereto and at least one optical fiber coupled into the integrated optical waveguide, wherein said micromachined PLC substrate comprising a silicon-on-insulator (SOI) wafer; and

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